

**Features:**

- **PRBS and user defined patterns**
- **DC coupled differential data inputs**  
(may also be used single ended or AC Coupled)
- **Full rate AC coupled clock input**  
(may also be used single ended)
- **Auto adjustment or manual adjustment of data to clock phase**
- **Auto synchronization to input pattern**
- **Front panel touch screen GUI or USB TMC computer control**

**PatternPro™** Line of  
 Serial Data Test Instruments



**Applications:**

- **25Gb/s and 28Gb/s testing for 100G Ethernet**
- **32Gb/s DPQPSK testing**
- **Semiconductor and component testing**
- **Design validation**
- **Production testing**

The Picosecond Pulse Labs' **SDA Model 13020** is a high performance, fully programmable benchtop and rack mount instrument. This error checker features a high-performance DC coupled analog front end that results in accurate error measurements of very high data rate signals.

**Specifications:**

Description:	Value/Details	Notes
Data rate	3 Gb/s to 32 Gb/s	
Data input Differential amplitude Single ended amplitude Termination voltage	100 mV to 1.0 V 100 mV to 1.0 V 0 V	<i>CML like input</i>
Clock input Single ended amplitude Differential amplitude	100 mV to 1.0 V 100 mV to 1.0 V	<i>AC coupled. Full rate.</i>
Clock to data phase adjustment	100 ps (-50 ps to +50 ps)	
Built-in PRBS patterns	$2^n - 1$ $n = 7, 9, 11, 15, 23, 31$	
User defined pattern depth	2 Mbit	<i>Single bit pattern resolution</i>

Ordering Information	
Model 13020-1	32Gb/s Programmable Error Checker, 1 Channel

**Contact Information**

Picosecond Pulse Labs  
 P.O. Box 44  
 Boulder, Colorado 80306, USA

Telephone: 303.209.8100  
 Fax: 303.447.2236  
 Email: info@picosecond.com

Website: www.picosecond.com